

**HAYNES BEFFEL & WOLFELD LLP**

INTELLECTUAL PROPERTY LAW

P.O. Box 366, 751 Kelly Street

Half Moon Bay, CA 94019

Phone 650-712-0340 Fax 650-712-0263

www.HMBay.com

MARK HAYNES  
ERNIE BEFFEL  
WARREN WOLFELD*Of Counsel*  
JIM HANN  
\*BILL KENNEDY  
KENTIA SUZUE

\*admitted in MA only

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FROM:	Mark A. Haynes/Kathryn Marley
CLIENT/MATTER:	MXIC 1553-3 (10/642,249)
DATE:	22 APRIL 2004

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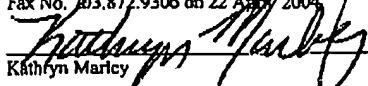
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Application Serial No. 10/642,249

MXIC 1553-3  
(P920080-2)

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Fax No. 703.872.9306 on 22 April 2004.

  
Kathryn Marley

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of Inventor:  
**Chih Chieh Yeh et al.**

Application No.: 10/642,249

Confirmation No. 1545

Filed: 15 August 2003

Title: **Method for Programming Programmable  
Eraseless Memory**

Group Art Unit: not assigned  
Examiner: not assigned

Customer No. 22470

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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**PRELIMINARY AMENDMENT**

Sir:

Please amend the above-referenced application as follows:

**In the Specification:**

Please replace paragraphs [0001] through [0003] with the following three paragraphs:

[0001] The present application claims the benefit of U.S. Provisional Patent Application No. 60/560,707, entitled PROGRAMMABLE RESISTOR ERASELESS MEMORY, invented by Yeh, et al., and filed on 21 July 2003 (converted application number 10/624,919 from non-provisional status to provisional status).

[0002] The present application is related to U.S. Patent Application No. 10/641,897, entitled METHOD FOR MANUFACTURING A PROGRAMMABLE ERASELESS MEMORY, invented by Yeh, et al., and filed on 15 August 2003.

[0003] The present application is related to U.S. Patent Application No. 10/641,846 entitled PROGRAMMABLE ERASELESS MEMORY, invented by Yeh, et al., and filed on 15 August 2003.

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